

Docket No.: M4065.0383/P383-C
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kie Y. Ahn et al.

Application No.: 10/759,079

Confirmation No.: 7174

Filed: January 20, 2004

Art Unit: 2818

For: **LOW-LOSS COPLANAR WAVEGUIDES AND
METHOD OF FABRICATION**

Examiner: D. Nhu

INFORMATION DISCLOSURE STATEMENT (IDS)

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir: ~

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed after the mailing date of a Final Office Action or Notice of Allowance, whichever occurred first, but on or before payment of the Issue Fee (37 C.F.R. § 1.97(d)). Applicants hereby petition that the Information Disclosure Statement be considered.

I hereby certify that no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

The reference listed on the attached form PTO/SB/08 has been cited in an Office Action dated July 27, 2005 in related patent Application Serial No. 09/835,643, filed on April 17, 2001. It is submitted that the Information Disclosure Statement is in compliance with 37 C.F.R. § 1.98 and the Examiner is respectfully requested to consider the listed reference.

DSMDB.1967804.1

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In accordance with 37 C.F.R. § 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made. In accordance with 37 C.F.R. § 1.97(h), the filing of this Information Disclosure Statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

Please charge our Credit Card in the amount of \$180.00 covering the fee set forth in 37 C.F.R. § 1.17(p). Credit Card Payment Form SB-2038, with a signature from an authorized cardholder, is enclosed. The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0383/P383-C. A duplicate copy of this paper is enclosed.

Dated: August 15, 2005

Respectfully submitted,

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PTO/SB/08a/b (07-05)

Approved for use through 07/31/2008. CMB 0851-0031

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Substitute for form 1449A/B/PTO				Complete If Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/759,079-Conf. #7174
				Filing Date	January 20, 2004
				First Named Inventor	Kie Y. Ahn
				Art Unit	2818
				Examiner Name	D. Nhu
Sheet	1	of	1	Attorney Docket Number	M4065.0383/P383-C

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

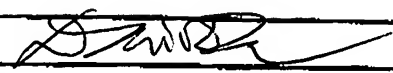
FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number-Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁴

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	S. Wolf, Ph.D., et al., "Silicon Processing for the VLSI Era," Chemical Vapor Deposition of Amorphous and Polycrystalline Films, Volume 1, Process Technology, pp. 182-183.	

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Examiner Signature		Date Considered	8/18/05
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DSMDB.1967817.1